

10/679124 ✓

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U.S. PATENT DOCUMENTS						
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				Filing Date	10/02/2003 10/02/2003 ✓
				First Named Inventor:	Suman Datta et al.
				Art Unit	Not yet assigned 2827 ✓
				Examiner Name	Not yet assigned Huan Hoang
				Attorney Docket Number	42P16682
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
HH		Evert Seevinck, "Static-Noise Margin Analysis of MOS SRAM Cells", IEEE Journal of Solid State Circuits, Vol. Sc-22, No. 5, October 1987, pp 748-754			
HH		Peter A. Stolk et al., "Modeling Statistical Dopant Fluctuations in MOS Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 9, September 1998, pp. 1960-1971			
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/227,068, Filed August 23, 2002			
checked		Robert S. Chau et al., "Tri-Gate Devices and Methods of Fabrication", Application Serial Number 10/367,263, Filed February 14, 2003			

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